

*2025/2/14

- SVMctl Software manual update. (Japanese only)

*2025/1/29

- FW/FPGA update. (svm_06_020_109_175_128_156_127.multi)

*2025/1/24 svm_06_020_109_175_128_156_127

- FW/FPGA update. (svm_06_020_109_174_128_155_127.multi)
- Updated Doc folder
- Updated Appl_x64 folder

*2024/01/26

- Updated Doc folder
 - Updated Control Library Manual (Japanese) from 2.0 to 2.1.
 - Updated SVMctl for x86 / x64. V1.4.6.3 -> V1.7.5.1
 - Updated SVMUpdater. V1.7.3.0 -> V1.8.0.1
 - Updated Control Library for x86 / x64. V1.2.0.0 -> V1.3.0.0
- Library update

*2022/08/19

- FPGA update-Updated the HDMI mode version from 1.47 to 1.50.

Changes

- Fixed a lane swap bug with less than 4 MIPI lanes.
- Added CRC-Check feature for MIPI long packet footer.
- Added timing optimization with manual/automatic suppression for transfers via frame memory.

This makes it possible to handle input images that were previously distorted due to the strict timing of V and H.

- Added support for external interrupts to FX3 as a reserved function for custom use.

*2022/08/05

- FPGA update-Updated the HDMI mode version from 1.46 to 1.47.

Changes

- When the camera resolution was smaller than the HDMI output resolution, it occurred at the start of video output of the board. Fixed the problem that the video slides from the bottom.
- Regarding the dummy line output function added in V1.45, we have made it work even when the H blank period of the camera image is short.

*2022/07/22

- Application update
 - Updated SVMctl. V1.4.5.0 -> V1.4.8.0
 - Updated SVMUpdater. V1.7.1.0 -> V1.7.3.0
 - Updated SVI05API.dll. V1.1.0.0 -> V1.2.0.0

*2022/07/06

- The version of FW has been upgraded.

- HDMI: 111 to 115
- UVC: 112 to 115

See FW_Release_Note.pdf for details.

- FPGA update-Updated the UVC mode version from 1.46 to 1.50.

Changes

- Fixed lane swap bugs below MIPI 4 Lane
- Added CRC check function for MIPI long packet footer
- Supports VCX (extended VC from 4 to 15 in addition to the conventional Virtual Channel 0 to 3) with MIPI
- Addition of timing optimization function by automatic / manual suppression for transfer via frame memory
- Supports external interruption to FX3

*2022/03/14

- Updated Doc folder
 - Updated SVM-06 Hardware specifications(Japanese) from 2.1 to 2.2.

*January 28th, 2022

- Application update
 - Updated SVMctl for x86 / x64. V1.4.6.0 -> V1.4.6.3
 - Updated NVCap for x86 / x64. V1.5.3.3 -> V1.5.3.7
- Driver update
 - Updated x86 / x64 sviu3drv.cat
 - Updated x64 sviu3drv.dll
 - Updated x86 / x64 sviu3drv.inf
- FW update
 - Updated HDMI mode version from 109 to 111.
 - Updated UVC mode version from 109 to 112.
 - Added support for revision 1.3. See FW_Release_Note.pdf for details. (Japanese)
 - The following documents were added to the new version of the FW_FPGA file.

*October 21, 2021

- Common
 - We upgraded the FPGA.
 - HDMI: 1.45 to 1.46
 - UVC: 1.45 to 1.46
 - Added support for revision 1.3. See FW_Release_Note.pdf for details. (Japanese)
 - We upgraded the FirmWare.
 - HDMI: 107 to 109
 - UVC: 107 to 109
 - Added support for revision 1.3.
 - The following documents were added to the new version of the FW_FPGA file.
- Updated Doc folder
 - Updated FPI-954 Hardware specifications.
 - Updated GMI-9296A-F Hardware specifications.
 - Updated GVI-4960 Hardware specifications(Japanese).

*2021/08/17

- Updated Doc folder
 - Updated SVM-06 Hardware specifications(Japanese) from 1.8 to 1.9.
- FW update
 - Updated HDMI mode version from 106 to 107.
 - Updated UVC mode version from 106 to 107.
- FPGA update
 - Updated HDMI mode version from 1.41 to 1.45.
 - Updated UVC mode version from 1.41 to 1.45.
- Bug fixes
 - Fixed an issue where noise can be added on a video frame when MIPI clock rate is around 1.5Gbps.
 - For customers using SVM-06 board released before 21/Aug, please update the board as follows:
 - (1) Set #7-8 of DIP SW (SW2) on SVM-06 board to {ON, OFF}.
 - (2) Launch SVMUpdate.exe, click (CrossLink Update) -> (Update Area 1), and select SVM06_XL_V24_210715.bit in FW_FPGA folder.
 - (3) Click (SVM Update) -> (Multi Update) and select svm-06_010_101_145_107_145_107_multi to update FPGA and firmware.

*2021/04/07

- Updated Doc folder
 - Updated SVM-06 Hardware specifications(Japanese) from 1.7 to 1.8.

- FW update
 - Updated HDMI mode version from 1.05 to 1.06.
 - Updated UVC mode version from 1.05 to 1.06.
- FPGA update
 - Updated HDMI mode version from 1.40 to 1.41.
 - Updated UVC mode version from 1.40 to 1.41.
- Addition of functions
 - Added the function of LED5 in UVC mode.

USB transfer is not in time, LED will light when the frame drop occurs due to buffer overflow.
It will be reset when the preview of the capture software (NVCap) starts.
- Bug fixes
 - Fixed an issue where NVCap could not preview for the first time when the PC was started with the SVM-06 connected to the PC.

*2021/03/23

- Updated Doc folder
 - Updated SVM-06 Hardware specifications(Japanese) from 1.5 to 1.7.
 - Updated SVMctl software manual(Japanese) from 10.4 to 10.5.
 - Added GMI-96716F-F hardware specifications(Japanese) to the SerDes folder.
- FW update
 - Updated HDMI mode version from 1.02 to 1.05.
 - Updated UVC mode version from 1.02 to 1.05.
- FPGA update
 - Updated HDMI mode version from 1.07 to 1.40.
 - Updated UVC mode version from 1.20 to 1.40.
- Application update
 - Updated SVMctl for x86 / x64. 1.4.4.0 to 1.4.5.0
 - Updated NVCap for x86 / x64. 1.5.2.3 to 1.5.3.0
- Addition of functions
 - Supports grayscale conversion of Raw8 format in UVC mode.
- Bug fixes
 - Fixed the error that occurred when transferring data of a specific size to the host.
 - For the phenomenon that was missing one line in case Long Packet and FE is extremely close,
was modified so that it can all lines capture.

*2020/11/04

- Fix a bug that prevented SVMctl application from starting.

*2020/10/30

- Updated folder structure.
- Renamed the update image file in FW_FPGA folder.
- Updated FW and FPGA in FW_FPGA folder.
 - Addition of automatic frame rate adjustment function according to USB transfer band
- Raw8, Raw20 and RGB24 format is now supported.
- Faster startup time